



j1UBL_ProjectDesign - j1UBL Project Design Component

v1.2

Features

- Single Project Design for multiple platforms
- Configuration all managed with component parameters

General Description

Our j1UBL bootloader exposed as a complete project design. Simple drop this component on a fresh TopDesign, add j1Types, configure the project design, compile, assign all pins etc. in CYDRW, recompile and program with an SWD programmer.

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No further documentation available at this time. Future releases will include full functional documentation.